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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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COMPLETE IF KNOWN	
Application Number	09/361,671
Confirmation Number	5810
Filing Date	July 27, 1999
First Named Inventor	Gail A. Alverson
Group Art Unit	2784
Examiner Name	

Sheet	1	of	7	Attorney Docket No.	324758003US1

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No.	U.S. Patent Document NUMBER	Kind Code (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
M.B	AA	5,301,325		Benson	4/5/94	
M.B	AB	5,333,3280		Ishikawa et al.	7/26/94	RECEIVED JUL 3 2001 Technology Center 2100
M.B	AC	5,450,575		Sites	9/12/95	
M.B	AD	5,598,560		Benson	1/28/97	
M.B	AE	5,652,889		Sites	7/29/97	
M.B	AF	5,768,592		Chang	6/16/98	

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M.B	AG	UK	GB2307760		AT&T Corp.	6/4/97		
M.B	AH	EP	EP0422945		International Business Machines	4/17/91		
M.B	AI	EP	EP0864979		Digital Equipment Corporation	9/16/98		
M.B	AJ	DE	DE19710252		Fujitsu Ltd.	2/26/98		
M.B	AK	EP	EP0537098		International Business Machines	4/14/93		
M.B	AL	EP	EP0855648		Texas Instruments, Inc.	7/29/98		
M.B	AM	EP	EP0455966		International Business Machines	11/13/91		
M.B	AN	DE	DE19710252		Fujitsu Ltd. (English Abstract)	2/26/98		
	AO							

OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

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M.B	AP	Burton Smith, "The End of Architecture," Keynote Address Presented at the 17th Annual Symposium on Computer Architecture, Seattle, Washington, May 29, 1990.	
M.B	AQ	Richard Korry et al., "Memory Management in the Tera MTA System," 1995.	
M.B	AR	Gail Alverson et al., "Processor Management in the Tera MTA System," 1995.	

EXAMINER	DATE CONSIDERED
[signature]	4/19/20

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MB	AC	6,029,005		Radigan	2/22/00	
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MB	AQ	Roy F. Touzeau, "A Fortran Compiler for the FPS-164 Scientific Computer," Proceedings of the ACM SIGPLAN '84 Symposium on Compiler Construction, SIGPLAN Notices Vol. 19, No. 6, June 1984.	
MB	AR	Mark A. Linton, "The Evolution of Dbx," USENIX Summer Conference, June 11-15, 1990.	

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Majid Banankhah	4/19/04

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M♭	AD	6,059,840		Click, Jr.	5/9/00	
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M♭	AH	6,009,269		Burrows et al.	12/28/99	
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M♭	AL	David Callahan and Burton Smith, "A Future-Based Parallel Language for a General-Purpose Highly-Parallel Computer, Languages and Compilers for Parallel Computing," MIT Press, 1990.	
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EXAMINER	Majid Banklea	DATE CONSIDERED	4/19/04

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Sheet	4	of	7	Attorney Docket No.	324758003US1

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MP	AA	5,961,639		Mallick et al.	10/5/99	
MP	AB	5,953,530		Rishi et al.	9/14/99	
MP	AC	5,903,730		Asai et al.	5/11/99	
MP	AD	5,901,315		Edwards et al.	5/4/99	
MP	AE	5,913,925		Kahle et al.	6/22/99	
MP	AF	5,887,166		Mallick et al.	3/23/99	
MP	AG	5,877,766		Bates et al.	3/2/99	
MP	AH	5,867,643		Sutton	2/2/99	
MP	AI	5,805,892		Nakajima	9/8/98	
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MP	AP	David Callahan, "Recognizing and Parallelizing Bounded Recurrences," August 1991.	
MP	AQ	D.H. Bailey et al., "The NAS Prallel Benchmarks - Summary and Preliminary Results," Numerical Aerodynamic Simulation (NAS) Systems Division, NASA Ames Research Center, California, 1991.	
MP	AR	Robert Alverson et al., "The Tera Computer System," Proceedings of 1990 ACM International Conference on Supercomputing, June 1990.	
MP	AS	Gail Alverson et al., "Scheduling on the Tera MTA," Job Scheduling Strategies for Parallel Processing, 1995.	
MP	AT	Burton Smith, "The Quest for General-Purpose Parallel Computing.	

EXAMINER	*Mozid Buakheak*	DATE CONSIDERED 4/19/04

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MB	AA	5,774,721		Robinson	6/30/98	
MB	AB	5,768,591		Robinson	6/16/98	
MB	AC	5,754,855		Miller et al.	5/19/98	
MB	AD	5,632,032		Ault et al.	5/20/97	
MB	AE	5,533,192		Hawley et al.	7/2/96	
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MB	AG	4,819,234		Huber	4/4/89	
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MB	AK	Preston Briggs and Keith D. Cooper, "Effective Partial Redundancy Elimination," ACM SIGPLAN Notices, Association for Computing Machinery, New York, Vol. 29, No. 6, June 1, 1994.	
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MB	AP	Gail Alverson et al., "Tera Hardware-Software Cooperation," Proceedings of Supercomputing 1997, San Jose, California, November 1997.	

EXAMINER	*Majid Banankhah*	DATE CONSIDERED	*4/18/04*

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Sheet	6	of	7	Attorney Docket No.	324758003US1

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M B	AA	5,812,811		Dubey et al.	9/22/98	
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	AC					

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	AD							

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M B	AH	Tomas Lang and Miquel Huguet, "Reduced Register Saving/Restoring in Single-Window Register Files," Computer Architecture News, Vol. 14, No. 3, June 1986.	
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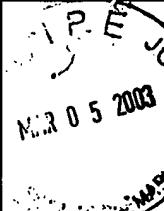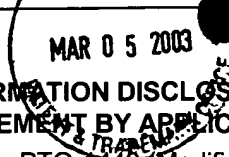| *EXAMINER INITIALS*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume0issue number(s), publisher, city and/or country where published.	T
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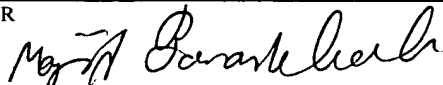
		COMPLETE IF KNOWN
	Application Number	09/361,671
	Confirmation Number	Unknown
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	Examiner Name	Unknown
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